

**GOA COLLEGE OF ENGINEERING**

FARMAGUDI, PONDA, GOA - 403401

16/05/2018 To 08/06/2018

**REGULAR EXAMINATION RESULT OF SEMESTER - IV (ELECTRICAL & ELECTRONICS) EXAMINATION HELD IN MAY-JUNE 2018 (REVISED COURSE)**

Seat No Roll no Category	A.R.E. Code	Male Female	Name of Candidate	Sessional Marks	Term Work Marks	Theory Obtd.	Theory Grace	Total Obtd.	Total Grace	Practical Obtd.	Practical Grace	Oral	Passes Fails	Total Sem-4	Total Grace	Total Sem-3	Grand Total Obtained	Remarks	PR Number
				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
<b>188147</b> <b>161103001</b> <b>GENERAL</b>	<b>A</b>	<b>M</b>	<b>ABDAR AKSHAY SHRIPATI</b>																<b>201610514</b>
			Numerical Techniques and Probabilty	17		75		92					P						
			Electrical Machines-II	10		67		77		17			P						
			Linear Integrated Circuits	12		54		66		15			P						
			Digital Integrated Circuits	11	20	54		85					P						
			Electrical Circuit Analysis and Synthesis	12		26		38					F						
			Electrical Power	12		71		83			21		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>						<b>494</b>		<b>470</b>		<b>964</b>	<b>FAILS</b>
<b>188148</b> <b>161103004</b> <b>GENERAL</b>	<b>A</b>	<b>M</b>	<b>BAKHLE AMOGH CHANDRASHEKAR</b>																<b>201610516</b>
			Numerical Techniques and Probabilty	10		48		58					P						
			Electrical Machines-II	13		63		76		18			P						
			Linear Integrated Circuits	10		56		66		18			P						
			Digital Integrated Circuits	16	23	49		88					P						
			Electrical Circuit Analysis and Synthesis	10		16		26					F						
			Electrical Power	10		60		70			22		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>						<b>442</b>		<b>441\$5</b>		<b>883\$5</b>	<b>FAILS</b>
<b>188149</b> <b>161103006</b> <b>GENERAL</b>	<b>A</b>	<b>M</b>	<b>BHAT KAUSHIK AMOL</b>																<b>201610518</b>
			Numerical Techniques and Probabilty	14		76		90					P						
			Electrical Machines-II	11		72		83		20			P						
			Linear Integrated Circuits	13		59		72		23			P						
			Digital Integrated Circuits	11	22	60		93					P						
			Electrical Circuit Analysis and Synthesis	12		40		52					P						
			Electrical Power	16		44		60			23		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>						<b>516</b>		<b>524</b>		<b>1040</b>	<b>FIRST CLASS</b>
<b>188150</b> <b>161103007</b> <b>GENERAL</b>	<b>A</b>	<b>F</b>	<b>BHAT TERESA</b>																<b>201610520</b>
			Numerical Techniques and Probabilty	25		92		117					P						
			Electrical Machines-II	22		76		98		22			P						
			Linear Integrated Circuits	19		87		106		23			P						
			Digital Integrated Circuits	24	24	78		126					P						
			Electrical Circuit Analysis and Synthesis	23		40		63					P						
			Electrical Power	24		67		91			23		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>						<b>669</b>		<b>657</b>		<b>1326</b>	<b>DISTINCTION</b>
<b>188151</b> <b>161103011</b> <b>OBC</b>	<b>A</b>	<b>M</b>	<b>DHARGALKAR ANANT ALIAS OMKAR KIRAN</b>																<b>201610526</b>
			Numerical Techniques and Probabilty	16		60		76					P						
			Electrical Machines-II	12		66		78		20			P						
			Linear Integrated Circuits	12		27		39		14			F						
			Digital Integrated Circuits	14	19	50		83					P						
			Electrical Circuit Analysis and Synthesis	14		16		30					F						
			Electrical Power	17		44		61			19		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>						<b>420</b>		<b>524</b>		<b>944</b>	<b>FAILS</b>
<b>188152</b> <b>161103012</b> <b>GENERAL</b>	<b>A</b>	<b>M</b>	<b>DHOND DATTARAJ DEEPAK</b>																<b>201610528</b>
			Numerical Techniques and Probabilty	20		90		110					P						
			Electrical Machines-II	12		80		92		22			P						
			Linear Integrated Circuits	15		77		92		24			P						
			Digital Integrated Circuits	13	21	64		98					P						
			Electrical Circuit Analysis and Synthesis	10		40		50					P						
			Electrical Power	17		65		82			16		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>						<b>586</b>		<b>629</b>		<b>1215</b>	<b>DISTINCTION</b>

#: ncc/nss/sport; \$ grace; a: absent; e: exemption; p: passes; f: fails; -+ marks carried over,\* passed in compartment  
discrepancies if any should be brought to the notice of academic incharge immediately

entered by \_\_\_\_\_ read by \_\_\_\_\_ checked by \_\_\_\_\_ principal

**REGULAR EXAMINATION RESULT OF SEMESTER - IV (ELECTRICAL & ELECTRONICS) EXAMINATION HELD IN MAY-JUNE 2018 (REVISED COURSE)**

Seat No	A.R.E.	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Total	Grand Total	Remarks	PR Number
Roll no	Code	Female		Marks	Marks	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace		Fails	Sem-4		Sem-3	Obtained		
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
<b>188153</b>	<b>A</b>	<b>M</b>	<b>DIAS LENNARD</b>																<b>201610530</b>
<b>161103014</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	20		69		89					P						
			Electrical Machines-II	16		76		92		22			P						
			Linear Integrated Circuits	17		74		91		20			P						
			Digital Integrated Circuits	18	24	70		112					P						
			Electrical Circuit Analysis and Synthesis	15		20		35					F						
			Electrical Power	18		51		69			20		P						
														<b>550</b>	<b>604</b>	<b>1154</b>		<b>FAILS</b>	
<b>188154</b>	<b>A</b>	<b>M</b>	<b>FERNANDES OSCAR AVELINO</b>																<b>201610531</b>
<b>161103015</b>																			
<b>OBC</b>			Numerical Techniques and Probabilty	14		63		77					P						
			Electrical Machines-II	10		58		68		15			P						
			Linear Integrated Circuits	10		42		52		15			P						
			Digital Integrated Circuits	10	20	40		70					P						
			Electrical Circuit Analysis and Synthesis	15		24		39					F						
			Electrical Power	11		54		65			16		P						
														<b>417</b>	<b>*414\$5</b>	<b>831\$5</b>		<b>FAILS</b>	
<b>188155</b>	<b>A</b>	<b>M</b>	<b>FERNANDES SHEWYN CYRUS</b>																<b>201610532</b>
<b>161103016</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	23		73		96					P						
			Electrical Machines-II	19		75		94		19			P						
			Linear Integrated Circuits	19		80		99		22			P						
			Digital Integrated Circuits	22	22	68		112					P						
			Electrical Circuit Analysis and Synthesis	18		26		44					F						
			Electrical Power	19		71		90			23		P						
														<b>599</b>	<b>624</b>	<b>1223</b>		<b>FAILS</b>	
<b>188156</b>	<b>A</b>	<b>M</b>	<b>GAD NIKHIL NILKANTH</b>																<b>201610535</b>
<b>161103017</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	12		43		55					P						
			Electrical Machines-II	11		56		67		18			P						
			Linear Integrated Circuits	10		15		25		14			F						
			Digital Integrated Circuits	12	20	24		56					F						
			Electrical Circuit Analysis and Synthesis	13		20		33					F						
			Electrical Power	14		43		57			17		P						
														<b>342</b>	<b>#10FAILS</b>	<b>.---</b>		<b>FAILS</b>	
<b>188157</b>	<b>A</b>	<b>M</b>	<b>GADEKAR SAHIL RAVINDRA</b>																<b>201610537</b>
<b>161103018</b>																			
<b>OBC</b>			Numerical Techniques and Probabilty	16		58		74					P						
			Electrical Machines-II	12		71		83		17			P						
			Linear Integrated Circuits	10		60		70		20			P						
			Digital Integrated Circuits	12	22	59		93					P						
			Electrical Circuit Analysis and Synthesis	12		26		38					F						
			Electrical Power	16		66		82			17		P						
														<b>494</b>	<b>464</b>	<b>958</b>		<b>FAILS</b>	
<b>188158</b>	<b>A</b>	<b>M</b>	<b>GAONKAR ASHOK ANANT</b>																<b>201610540</b>
<b>161103019</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	19		78		97					P						
			Electrical Machines-II	18		64		82		20			P						
			Linear Integrated Circuits	12		70		82		18			P						
			Digital Integrated Circuits	17	20	55		92					P						
			Electrical Circuit Analysis and Synthesis	12		40		52					P						
			Electrical Power	15		60		75			18		P						
														<b>536</b>	<b>512</b>	<b>1048</b>		<b>FIRST CLASS</b>	

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Seat No	A.R.E.	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Total	Grand Total	Remarks	PR Number
Roll no	Code	Female		Marks	Marks	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace		Fails	Sem-4		Sem-3	Obtained		
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
<b>188159</b>	<b>A</b>	<b>M</b>	<b>GAONKAR OMKAR DINESH</b>																<b>201610542</b>
<b>161103020</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	16		56		72					P						
			Electrical Machines-II	10		40		50		18			P						
			Linear Integrated Circuits	11		56		67		18			P						
			Digital Integrated Circuits	15	21	49		85					P						
			Electrical Circuit Analysis and Synthesis	12		22		34					F						
			Electrical Power	12		48		60			14		P						
														<b>418</b>		<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
<b>188160</b>	<b>A</b>	<b>M</b>	<b>GAONKAR PRAJYOT PRABHAKAR</b>																<b>201610556</b>
<b>161103021</b>																			
<b>ST</b>			Numerical Techniques and Probabilty	14		64		78					P						
			Electrical Machines-II	10		78		88		20			P						
			Linear Integrated Circuits	11		52		63		10			P						
			Digital Integrated Circuits	10	21	50		81					P						
			Electrical Circuit Analysis and Synthesis	10		20		30					F						
			Electrical Power	12		50		62			13		P						
														<b>445</b>		<b>471</b>	<b>916</b>		<b>FAILS</b>
<b>188161</b>	<b>A</b>	<b>M</b>	<b>GAONKER SHUBHAM MHABLU</b>																<b>201610558</b>
<b>161103022</b>																			
<b>ST</b>			Numerical Techniques and Probabilty	17		63		80					P						
			Electrical Machines-II	10		48		58		19			P						
			Linear Integrated Circuits	11		27		38		18			F						
			Digital Integrated Circuits	14	22	29		65					F						
			Electrical Circuit Analysis and Synthesis	12		24		36					F						
			Electrical Power	16		42		58			21		P						
														<b>393</b>		<b>427\$5</b>	<b>820\$5</b>		<b>FAILS</b>
<b>188162</b>	<b>A</b>	<b>M</b>	<b>GAUNEKER VISHWAMBHAR RAMDAS</b>																<b>201610562</b>
<b>161103023</b>																			
<b>ST</b>			Numerical Techniques and Probabilty	13		62		75					P						
			Electrical Machines-II	19		66		85		15			P						
			Linear Integrated Circuits	13		54		67		15			P						
			Digital Integrated Circuits	12	21	53		86					P						
			Electrical Circuit Analysis and Synthesis	13		20		33					F						
			Electrical Power	12		60		72			19		P						
														<b>467</b>		<b>408\$7</b>	<b>875\$7</b>		<b>FAILS</b>
<b>188163</b>	<b>A</b>	<b>M</b>	<b>KAMBLI MANTHAN MAHADEV</b>																<b>201610567</b>
<b>161103026</b>																			
<b>OBC</b>			Numerical Techniques and Probabilty	11		35	\$5	46	\$5				P						
			Electrical Machines-II	10		36	\$4	46	\$4	16			P						
			Linear Integrated Circuits	10		11		21		12			F						
			Digital Integrated Circuits	12	17	35	\$5	64	\$5				P						
			Electrical Circuit Analysis and Synthesis	11		10		21					F						
			Electrical Power	11		35		46			14		F						
														<b>286</b>	<b>\$14</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
<b>188164</b>	<b>A</b>	<b>M</b>	<b>KANTAK SAURAV SUDIN</b>																<b>201611233</b>
<b>161103028</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	11		54		65					P						
			Electrical Machines-II	10		37	\$3	47	\$3	17			P						
			Linear Integrated Circuits	10		40		50		18			P						
			Digital Integrated Circuits	12	17	40		69					P						
			Electrical Circuit Analysis and Synthesis	10		24		34					F						
			Electrical Power	10		36	\$4	46	\$4		14		P						
														<b>360</b>	<b>\$7</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>



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Seat No	A.R.E.	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Total	Grand Total	Remarks	PR Number
Roll no	Code	Female		Marks	Marks	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace		Fails	Sem-4		Sem-3	Obtained		
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
188171	A	M	<b>NAIK PRABHAV RAJENDRA</b>																201610595
161103037																			
<b>GENERAL</b>																			
			Numerical Techniques and Probabilty	10		51		61					P						
			Electrical Machines-II	10		42		52		18			P						
			Linear Integrated Circuits	10		21		31		18			F						
			Digital Integrated Circuits	13	19	24		56					F						
			Electrical Circuit Analysis and Synthesis	13		22		35					F						
			Electrical Power	14		25		39			14		F						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 FAIL</b>				<b>324</b>		<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
188172	A	F	<b>NAIK PRIYANKA PANDURANG</b>																201610599
161103038																			
<b>OBC</b>																			
			Numerical Techniques and Probabilty	11		53		64					P						
			Electrical Machines-II	12		36	\$4	48	\$4	17			P						
			Linear Integrated Circuits	9		20		29		10			F						
			Digital Integrated Circuits	10	21	30		61					F						
			Electrical Circuit Analysis and Synthesis	12		20		32					F						
			Electrical Power	13		46		59			12		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 FAIL</b>				<b>332</b>	<b>\$4</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
188173	A	M	<b>NAIK SAHIL GAJANAN</b>																201610602
161103039																			
<b>OBC</b>																			
			Numerical Techniques and Probabilty	13		78		91					P						
			Electrical Machines-II	11		45		56		19			P						
			Linear Integrated Circuits	13		55		68		18			P						
			Digital Integrated Circuits	20	24	72		116					P						
			Electrical Circuit Analysis and Synthesis	10		16		26					F						
			Electrical Power	13		35	\$5	48	\$5		20		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>462</b>	<b>\$5</b>	<b>495\$5</b>	<b>957\$10</b>		<b>FAILS</b>
188174	A	M	<b>NAIK SAISH KISHOR</b>																201610605
161103040																			
<b>OBC</b>																			
			Numerical Techniques and Probabilty	13		60		73					P						
			Electrical Machines-II	10		50		60		16			P						
			Linear Integrated Circuits	10		40		50		10			P						
			Digital Integrated Circuits	10	18	29		57					F						
			Electrical Circuit Analysis and Synthesis	10		12		22					F						
			Electrical Power	14		40		54			15		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 FAIL</b>				<b>357</b>		<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
188175	A	F	<b>NAIK SONIA ANAND</b>																201610607
161103041																			
<b>OBC</b>																			
			Numerical Techniques and Probabilty	15		69		84					P						
			Electrical Machines-II	11		37	\$3	48	\$3	17			P						
			Linear Integrated Circuits	10		49		59		15			P						
			Digital Integrated Circuits	14	20	40		74					P						
			Electrical Circuit Analysis and Synthesis	14		20		34					F						
			Electrical Power	14		60		74			13		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>418</b>	<b>\$3</b>	<b>*421\$5</b>	<b>839\$8</b>		<b>FAILS</b>
188176	A	F	<b>NAIK TEJASWINI TULSHIDAS</b>																201610609
161103042																			
<b>OBC</b>																			
			Numerical Techniques and Probabilty	14		59		73					P						
			Electrical Machines-II	13		49		62		16			P						
			Linear Integrated Circuits	10		50		60		10			P						
			Digital Integrated Circuits	15	22	51		88					P						
			Electrical Circuit Analysis and Synthesis	11		40		51					P						
			Electrical Power	15		44		59			12		P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>431</b>		<b>*378\$5</b>	<b>809\$5</b>		<b>PASSES *</b>

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Seat No	A.R.E.	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Total	Grand Total	Remarks	PR Number
Roll no	Code	Female		Marks	Marks	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace		Fails	Sem-4		Sem-3	Obtained		
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
188177 161103043 GENERAL	A	M	<b>NARAYANPUR WASIM AKRAM ABDUL</b>																201610611
			Numerical Techniques and Probabilty	12		53		65					P						
			Electrical Machines-II	14		49		63		17			P						
			Linear Integrated Circuits	10		40		50		18			P						
			Digital Integrated Circuits	12	18	35	\$5	65	\$5				P						
			Electrical Circuit Analysis and Synthesis	15		24		39					F						
			Electrical Power	15		42		57				15	P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>389</b>	<b>\$5</b>	<b>406\$5</b>	<b>795\$10</b>		<b>FAILS</b>
188178 161103044 GENERAL	A	M	<b>PARAB AJINKYA BALA</b>																201611231
			Numerical Techniques and Probabilty	11		54		65					P						
			Electrical Machines-II	10		36	\$4	46	\$4	16			P						
			Linear Integrated Circuits	10		21		31		10			F						
			Digital Integrated Circuits	11	17	41		69					P						
			Electrical Circuit Analysis and Synthesis	12		20		32					F						
			Electrical Power	14		50		64				15	P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 FAIL</b>				<b>348</b>	<b>\$4</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
188179 161103045 GENERAL	A	M	<b>PATIL SAURABH PARASHARAM</b>																201610614
			Numerical Techniques and Probabilty	19		78		97					P						
			Electrical Machines-II	12		68		80		23			P						
			Linear Integrated Circuits	17		64		81		20			P						
			Digital Integrated Circuits	19	21	59		99					P						
			Electrical Circuit Analysis and Synthesis	17		40		57					P						
			Electrical Power	17		64		81				19	P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>557</b>		<b>602</b>	<b>1159</b>		<b>FIRST CLASS</b>
188180 161103047 GENERAL	A	F	<b>PRATIKSHA NAGRAJ PATIL</b>																201610767
			Numerical Techniques and Probabilty	20		78		98					P						
			Electrical Machines-II	14		66		80		20			P						
			Linear Integrated Circuits	11		59		70		19			P						
			Digital Integrated Circuits	17	24	40		81					P						
			Electrical Circuit Analysis and Synthesis	10		40		50					P						
			Electrical Power	18		36	\$4	54	\$4			20	P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>492</b>	<b>\$4</b>	<b>545</b>	<b>1037\$4</b>		<b>FIRST CLASS</b>
188181 161103049 OBC	A	F	<b>SAKROJI VEDA VINAYAK</b>																201610618
			Numerical Techniques and Probabilty	15		63		78					P						
			Electrical Machines-II	10		54		64		17			P						
			Linear Integrated Circuits	10		35	\$5	45	\$5	14			P						
			Digital Integrated Circuits	12	20	40		72					P						
			Electrical Circuit Analysis and Synthesis	11		22		33					F						
			Electrical Power	15		42		57				13	P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>393</b>	<b>\$5</b>	<b>*417\$2</b>	<b>810\$7</b>		<b>FAILS</b>
188182 161103050 GENERAL	A	M	<b>SALUNKHE SIDDHANTA SHRIKANT</b>																201610622
			Numerical Techniques and Probabilty	20		70		90					P						
			Electrical Machines-II	13		46		59		18			P						
			Linear Integrated Circuits	12		55		67		16			P						
			Digital Integrated Circuits	13	16	70		99					P						
			Electrical Circuit Analysis and Synthesis	19		20		39					F						
			Electrical Power	12		39	\$1	51	\$1			17	P						
						<b>SEM-1 PASS</b>		<b>SEM-2 PASS</b>		<b>SEM-3 PASS</b>				<b>456</b>	<b>\$1</b>	<b>467</b>	<b>923\$1</b>		<b>FAILS</b>







**REGULAR EXAMINATION RESULT OF SEMESTER - IV (ELECTRICAL & ELECTRONICS) EXAMINATION HELD IN MAY-JUNE 2018 (REVISED COURSE)**

Seat No	A.R.E.	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Total	Grand Total	Remarks	PR Number
Roll no	Code	Female		Marks	Marks	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace		Fails	Sem-4		Sem-3	Obtained		
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
188195	A	M	<b>NAIK PRAJYOT RAJENDRA</b>																201610598
161103068			OBC																
			Numerical Techniques and Probabilty	15		58		73					P						
			Electrical Machines-II	11		51		62		20			P						
			Linear Integrated Circuits	14		52		66		15			P						
			Digital Integrated Circuits	17	20	46		83					P						
			Electrical Circuit Analysis and Synthesis	14		40		54					P						
			Electrical Power	14		35	\$5	49	\$5			15	P						
						<b>SEM-1 PASS</b>				<b>SEM-2 PASS</b>				<b>437</b>	<b>\$5</b>	<b>433\$10</b>	<b>870\$15</b>		<b>SECOND CLASS</b>
188196	A	M	<b>BANDEKAR AMEY SATISH</b>																201611239
161103069			GENERAL																
			Numerical Techniques and Probabilty	12		47		59					P						
			Electrical Machines-II	10		49		59		17			P						
			Linear Integrated Circuits	10		18		28		10			F						
			Digital Integrated Circuits	12	19	30		61					F						
			Electrical Circuit Analysis and Synthesis	11		20		31					F						
			Electrical Power	10		14		24				16	F						
						<b>SEM-1 PASS</b>				<b>SEM-2 PASS</b>				<b>305</b>		<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
188197	A	F	<b>KERKAR NEHA NARAYAN</b>																201610937
161103070			ST																
			Numerical Techniques and Probabilty	15		63		78					P						
			Electrical Machines-II	10		38	\$2	48	\$2	16			P						
			Linear Integrated Circuits	11		41		52		15			P						
			Digital Integrated Circuits	12	23	56		91					P						
			Electrical Circuit Analysis and Synthesis	10		24		34					F						
			Electrical Power	10		35	\$5	45	\$5			13	P						
						<b>SEM-1 PASS</b>				<b>SEM-2 PASS</b>				<b>392</b>	<b>\$7</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
188198	A	M	<b>MARGAJ SHUBHAM PRAKASH</b>																201611242
161103071			GENERAL																
			Numerical Techniques and Probabilty	10		46		56					P						
			Electrical Machines-II	10		47		57		16			P						
			Linear Integrated Circuits	10		40		50		10			P						
			Digital Integrated Circuits	10	19	40		69					P						
			Electrical Circuit Analysis and Synthesis	10		40		50					P						
			Electrical Power	10		36	\$4	46	\$4			13	P						
						<b>SEM-1 PASS</b>				<b>SEM-2 FAIL</b>				<b>367</b>	<b>\$4</b>	<b>FAILS</b>	<b>.---</b>		<b>R.R.L.E.</b>
188199	A	M	<b>KAMAT NEHAL RAMCHANDRA</b>																201705111
171203001			GENERAL																
			Numerical Techniques and Probabilty	15		46		61					P						
			Electrical Machines-II	10		65		75		17			P						
			Linear Integrated Circuits	10		47		57		20			P						
			Digital Integrated Circuits	12	20	41		73					P						
			Electrical Circuit Analysis and Synthesis	12		20		32					F						
			Electrical Power	15		38	\$2	53	\$2			13	P						
						<b>SEM-1 -</b>				<b>SEM-2 -</b>				<b>401</b>	<b>\$2</b>	<b>387\$9</b>	<b>788\$11</b>		<b>FAILS</b>
188200	A	F	<b>KARMALKAR SHARVANI SANJAY</b>																201705139
171203002			GENERAL																
			Numerical Techniques and Probabilty	14		36	\$4	50	\$4				P						
			Electrical Machines-II	10		50		60		19			P						
			Linear Integrated Circuits	10		23		33		10			F						
			Digital Integrated Circuits	15	18	44		77					P						
			Electrical Circuit Analysis and Synthesis	11		8		19					F						
			Electrical Power	10		28		38				15	F						
						<b>SEM-1 -</b>				<b>SEM-2 -</b>				<b>321</b>	<b>\$4</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>

**REGULAR EXAMINATION RESULT OF SEMESTER - IV (ELECTRICAL & ELECTRONICS) EXAMINATION HELD IN MAY-JUNE 2018 (REVISED COURSE)**

Seat No	A.R.E.	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Total	Grand Total	Remarks	PR Number
Roll no	Code	Female		Marks	Marks	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace		Fails	Sem-4		Sem-3	Obtained		
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
<b>188201</b>	<b>A</b>	<b>M</b>	<b>KHANDOLKAR TEJNATH UTTAM</b>																<b>201704892</b>
<b>171203003</b>																			
<b>ST</b>			Numerical Techniques and Probabilty	10		22		32					F						
			Electrical Machines-II	10		57		67		15			P						
			Linear Integrated Circuits	10		40		50		18			P						
			Digital Integrated Circuits	10	21	35	\$5	66	\$5				P						
			Electrical Circuit Analysis and Synthesis	10		22		32					F						
			Electrical Power	15		43		58				12	P						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 FAIL</b>				<b>350</b>	<b>\$5</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
<b>188202</b>	<b>A</b>	<b>F</b>	<b>KOTTALGI VIBHA</b>																<b>201704929</b>
<b>171203004</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	22		76		98					P						
			Electrical Machines-II	21		60		81		19			P						
			Linear Integrated Circuits	16		65		81		22			P						
			Digital Integrated Circuits	24	24	61		109					P						
			Electrical Circuit Analysis and Synthesis	21		24		45					F						
			Electrical Power	15		40		55				23	P						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 PASS</b>				<b>533</b>		<b>473</b>	<b>1006</b>		<b>FAILS</b>
<b>188203</b>	<b>A</b>	<b>M</b>	<b>NAIK SAEEL PRAKASH</b>																<b>201705112</b>
<b>171203005</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	14		43		57					P						
			Electrical Machines-II	10		57		67		19			P						
			Linear Integrated Circuits	10		47		57		20			P						
			Digital Integrated Circuits	11	20	21		52					F						
			Electrical Circuit Analysis and Synthesis	10		40		50					P						
			Electrical Power	11		35	\$5	46	\$5			13	P						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 PASS</b>				<b>381</b>	<b>\$5</b>	<b>*387\$8</b>	<b>768\$13</b>		<b>FAILS</b>
<b>188204</b>	<b>A</b>	<b>M</b>	<b>NAIK SAISH MADHU</b>																<b>201705104</b>
<b>171203006</b>																			
<b>OBC</b>			Numerical Techniques and Probabilty	14		44		58					P						
			Electrical Machines-II	10		57		67		19			P						
			Linear Integrated Circuits	11		44		55		18			P						
			Digital Integrated Circuits	11	19	49		79					P						
			Electrical Circuit Analysis and Synthesis	11		12		23					F						
			Electrical Power	12		40		52				13	P						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 PASS</b>				<b>384</b>		<b>*379\$10</b>	<b>763\$10</b>		<b>FAILS</b>
<b>188205</b>	<b>A</b>	<b>M</b>	<b>REBELLO JEREMIAH MICHAEL FILLU</b>																<b>201705116</b>
<b>171203007</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	11		26		37					F						
			Electrical Machines-II	10		26		36		17			F						
			Linear Integrated Circuits	10		40		50		22			P						
			Digital Integrated Circuits	11	18	35	\$5	64	\$5				P						
			Electrical Circuit Analysis and Synthesis	10		16		26					F						
			Electrical Power	10		12		22				16	F						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 FAIL</b>				<b>290</b>	<b>\$5</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
<b>188206</b>	<b>A</b>	<b>M</b>	<b>SANGEKAR DARSHAN DAMODAR</b>																<b>201704889</b>
<b>171203008</b>																			
<b>GN</b>			Numerical Techniques and Probabilty	16		59		75					P						
			Electrical Machines-II	10		59		69		17			P						
			Linear Integrated Circuits	13		56		69		20			P						
			Digital Integrated Circuits	15	22	43		80					P						
			Electrical Circuit Analysis and Synthesis	12		24		36					F						
			Electrical Power	15		40		55				15	P						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 PASS</b>				<b>436</b>		<b>*415\$4</b>	<b>851\$4</b>		<b>FAILS</b>

**REGULAR EXAMINATION RESULT OF SEMESTER - IV (ELECTRICAL & ELECTRONICS) EXAMINATION HELD IN MAY-JUNE 2018 (REVISED COURSE)**

Seat No	A.R.E.	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Total	Grand Total	Remarks	PR Number
Roll no	Code	Female		Marks	Marks	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace		Fails	Sem-4		Sem-3	Obtained		
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace			850	Grace	850			
<b>188207</b>	<b>A</b>	<b>M</b>	<b>SAWANT KAUSHAL RAVI</b>																<b>201711962</b>
<b>171203009</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	14		53		67					P						
			Electrical Machines-II	11		73		84		19			P						
			Linear Integrated Circuits	12		48		60		16			P						
			Digital Integrated Circuits	17	22	45		84					P						
			Electrical Circuit Analysis and Synthesis	13		22		35					F						
			Electrical Power	15		30		45			16		F						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 PASS</b>				<b>426</b>		<b>459\$5</b>	<b>885\$5</b>		<b>FAILS</b>
<b>188208</b>	<b>A</b>	<b>M</b>	<b>SHAIK MOHAMMAD SAFIR</b>																<b>201704891</b>
<b>171203010</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	10		35	\$5	45	\$5				P						
			Electrical Machines-II	10		41		51		16			P						
			Linear Integrated Circuits	10		42		52		20			P						
			Digital Integrated Circuits	11	21	26		58					F						
			Electrical Circuit Analysis and Synthesis	11		18		29					F						
			Electrical Power	13		15		28			20		F						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 FAIL</b>				<b>319</b>	<b>\$5</b>	<b>FAILS</b>	<b>.---</b>		<b>FAILS</b>
<b>188209</b>	<b>A</b>	<b>M</b>	<b>SHET PARSEKAR PRANAV VITHAL PRAKASH</b>																<b>201704890</b>
<b>171203011</b>																			
<b>GENERAL</b>			Numerical Techniques and Probabilty	12		47		59					P						
			Electrical Machines-II	10		28		38		18			F						
			Linear Integrated Circuits	10		25		35		10			F						
			Digital Integrated Circuits	10	19	27		56					F						
			Electrical Circuit Analysis and Synthesis	11		18		29					F						
			Electrical Power	14		35	\$5	49	\$5		13		P						
						<b>SEM-1 -</b>		<b>SEM-2 -</b>		<b>SEM-3 PASS</b>				<b>307</b>	<b>\$5</b>	<b>*376\$3</b>	<b>683\$8</b>		<b>FAILS</b>

Declared on : \_\_\_\_\_  
 Entered by : \_\_\_\_\_  
 Read by : \_\_\_\_\_  
 Checked by : \_\_\_\_\_

**Principal**